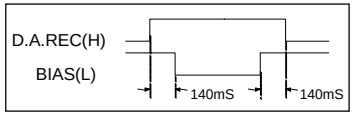
No.	Port Name	I/O	Description	P-OFF	P-Save	P-FAIL	Reset
21	ROTARY.SW	O	Output terminal for rotary switch.	Low	Low	Low	Low
22	H.A.SW	O	Output terminal for HEAD AMP switch.	Low	Low	Low	Low
23	ENVE.SELECT	I	Input terminal for enverope selection.	Low	Low	Low	Low
24	VIDEO.H.SW	O	Output terminal for video HEAD switch.	Low	Low	Low	Low
25	A.H.SW	O	Output terminal for FM audio HEAD switch.	Low	Low	Low	Low
26	A.DEF(H)	O	Audio Mute output terminal for tuner and decoder. [High].....During VCR mode is NORMAL PLAY and VV mode.	Low	Low	Low	Low
27	PROG.ON(H)	O	I/O control to switch IIC bus for LW-programmer. H:IIC bus is connected to AV2. L:IIC bus is not connected to AV2.	Low	Low	Low	Low
28	AVPB-H(L)	O	"PB-(H)"output terminal for AV2 *Details, refer to 21pin control specifications. *The signal output from Pin-8 of 21pin terminal is shown below.	High	High	Low	High
29	AVPB-M(H)	O		Low	Low	Low	Low
30	GND(D)	--	GND	---	---	---	---
31	20MHz.IN	I	Main oscillator input	---	STOP	STOP	---
32	20MHz.OUT	O	Main oscillator output	---	STOP	STOP	---
33	+5V(D)	I	5V input terminal	---	---	---	---
34	32KHz IN	I	Sub oscillator input	---	---	---	---
35	32KHz OUT	O	Sub oscillator output	---	---	---	---
36	NC (VCONT)	I	Video Control	---	---	---	---
37	V EE(L)	O	Output terminal for switch between EE and VV.	Low	Low	Low	Low
38	D REC(H)	O	Output terminal for video recording current control.	Low	Low	Low	Low
39	D.A.REC(H)	O	Control terminal for normal audio recording current.	Low	Low	Low	Low
40	BIAS(L)	O	Bias oscillation Control terminal for Linear audio recording current.	High	Hi-Z	Hi-Z	High
41	FM.MUTE(H)	O	Output terminal for audio mute control.	High	High	Low	High
42	FULL ERASE(L)	O	Output terminal for control FULL ERASE HEAD.	Low	Low	Low	Low
43	POWER_KEY	I	Input terminal for the unit power button. It switches ON to OFF and/or OFF to ON when down edge is detected.	In	In	In	In
44	POS.SW3	I	Input terminal for mecha position.	In	In	In	In
45	POS.SW2	I	Input terminal for mecha position.	In	In	In	In
46	POS.SW1	I	Input terminal for mecha position.	In	In	In	In
47	POWER OFF(H)	O	ON/OFF Control terminal for power circuit. Power off condition: "High" is existed. ("Low" is existed when the mechanism and/or circuit operation is activated.)	High	High	Low	High
48	FIP(L)	O	Output terminal for FIP on/off control signal. ("Low" is existed when the power off condition under the "FIP OFF" setting.)	Not fix	High	Low	Low
49	P50 IN	I	Input terminal for Project 50 serial signal.	In	In	In	In
50	LC.OSC IN	I	The oscillation input terminal for OSD dot clock.	---	---	---	---

pin8 \ Name	AVPB-H(L)	AVPBM-(H)
High	L	Hi-Z
Middle	H	Hi-Z
Low	H	L